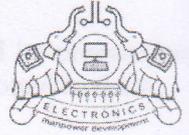




COLLEGE OF APPLIED SCIENCE KUZHALMANNAM
(Managed by I.H.R.D. Thiruvananthapuram. A Govt. of Kerala
undertaking & Affiliated to University of Calicut)
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Quotation Notice

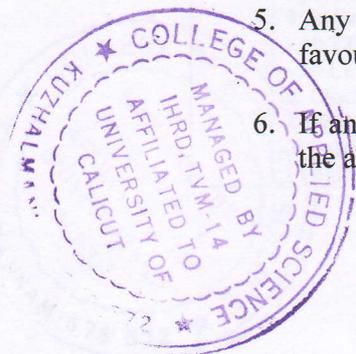
No.272/PLANFUND/2020-21/CASK/2

DATED 7/12/2020

Sealed quotations are invited for the supply of the materials (Electronics Lab Equipment) specified in the schedule attached below over leaf. The rates quoted should be for delivery of the articles at the places mentioned below the schedule. Necessary superscription, due date for the receipt of quotations, date up to which the rates will have to remain firm for acceptance and the name and address of officer to whom the quotation is to be sent are noted below. Any quotation received after the time fixed on the due date is liable to be rejected. The maximum period required for delivery of the articles should also be mentioned.

THE ACCEPTANCE OF QUOTATIONS WILL BE SUBJECT TO THE FOLLOWING CONDITIONS:

1. Acceptance of the quotation constitutes a concluded contract. Nevertheless, The successful tendered must within a fortnight / a month after the acceptance of his quotation furnish 5 percent amount of the contract as security deposited and execute an agreement at his own cost for the satisfactory fulfillment of the contract, if so required.
2. Withdrawal from the quotation after it is accepted or failure to supply within a specified time or according to specification will entail cancellations of the order and purchases being made at the officer's expense from elsewhere, any loss incurred thereby being payable by the defaulting party. In such an event the government reserve also the right to remove the defaulter's name from the list of government suppliers permanently or for a specified number of years
3. Samples, duly listed should be forwarded wherever possible under separate cover and the unapproved samples got back as early as possible by the officers at their own expenses and the government will in no case be liable for any expense on account of the value of the samples or their transport charges etc. In case, the samples are sent by railway the railway receipt should be sent separately and not along with the quotation since the quotation will be opened only on the appointed day demurrage will have to be paid if the railway parcels are not cleared in time .The approved samples may or may not be returned at the discretion of the undersigned. Samples sent by v.p.p or freight to pay will not be accepted.
4. No representation for enhancement of price once accepted will be considered during the currency of the contract.
5. Any attempt of the part of tenders or their agents to influence the officers concerned in their favour by personal canvassing will disqualify the tenders.
6. If any license or permit is required tenderers must specify it in their quotation and also state the authority to whom application is to be made.



7. The quotation may be for the entire or part supplies. But the tenders should be prepared to carry out such portion of the supplies included in their quotation as may be allotted to them.
8. The prices quoted should be included of all taxes, duties, cesses, etc. which are or may become payable by the contractor under existing or future laws or rules of the country of origin / supply or delivery during the course of execution of the contract.
9. The tenders shall quote also the percentage of rebate (discount) offered by them in case the payment is made promptly within fifteen days / within one month of taking delivery of stores.
10. Special conditions, if any printed on the quotation sheets of the tenders or attached with the tenders will not be applicable to the contract unless they are expressly accepted in writing by the purchaser.

Superscription on the envelope required : "Quotation for supply of Electronics Equipment"

Due date and time for receipt of quotations : 22/12/2020 2:00 pm

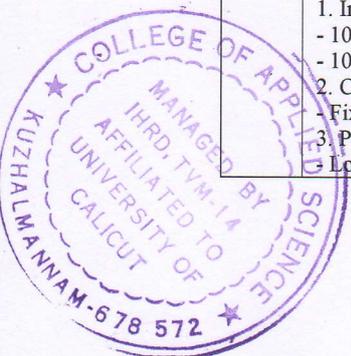
Date and time for opening of quotations : 22/12/2020 3:00 pm

Date up to which the rates are to remain firm the acceptance : 3 months

Designation and address of officer to

whom the quotation is to be addressed : PRINCIPAL , COLLEGE OF APPLIED SCIENCE
KUZHALMANNAM ,KOTTAYI P O ,
CHEMBRAKULAM , PALAKKAD DIST 678572

SNo	Item with Specification	Quantity
1	<p>Function generator -</p> <p><i>TECHNICAL SPECIFICATIONS:-</i></p> <p>(DDS BASED FG WITH STANDARD FUNCTIONS & MODULATION AM/FM/PWM/ASK/FSK/SWEEP) Output waveforms : Sine, Square, Triangle and Pulse& TTL Output : Socket-BNC, Amplitude – 20 V p-p with 50 ohms impedance Frequency Range : 0.1 Hz to 3MHz (SINE,SQR and PULSE) ;0.1 Hz to 1 MHz (TRI) Selected in 7 steps. Variable Control Display : LCD, micro controlled- menu Keys with freq and amplitude display Modulation : AM, FM, PWM ,ASK,FSK and SWEEP(DC TO 20 KHz)- INTERNAL/EXTERNAL Mod input : 5Vpp(maximum) DC Offset : +/- 5V continuously adjustable, attenuated by step Attenuator. Attenuation : 0 db, 20 db, 40 db, 60 db. Level Flatness : 0.2dB from 10 Hz to 1MHz Stability : + 2pm aging +PPM/year amplitude Sine Wave Distortion : 0.5% maximum at 20Vp-p 0.1% maximum at 2V p-p (FO= 20 KHz) Square/Pulse Rise & Fall Time : Less than 50nS at full rated Output. Pulse Duty Cycle Variation : 0% to 100% (10Hz – 1 MHz) 25% to 75% (> 1 MHz) Minimum pulse width : 75 ns Triangle Linearity Error : 0.25 (FO = KHz, 5% to 95%) Output Amplifier Protection : Protected against short circuit and + 15V overload. Power Requirement : 230 V AC + 10%, 50Hz, 10VA. Int. Mod. Freq. : 2Hz to 20 KHz (SIN/SQU/TRI) Carrier Freq. B.W. : Up to 1MHz</p>	2Nos
2	<p>1. Digital IC Trainer kit -</p> <p>Specifications</p> <ol style="list-style-type: none"> 1. Indicators <ul style="list-style-type: none"> - 10 TTL/CMOS Logic Level Inputs with Dual Color LED indication for logic low and logic high - 10 LED for output indication. 2. Clock Generator <ul style="list-style-type: none"> - Fixed Clock generation of 1Hz, 10Hz, 100Hz, 1 KHz, 10 KHz, and 100 KHz & 1 MHz. 3. Pulse Generator <ul style="list-style-type: none"> - Logic Pulsar provides single pole double throw bounce less pulses of Low to High and High to Low 	3Nos



	<p>transitions.</p> <p>4. Logic Probe</p> <ul style="list-style-type: none"> - Logic Probe facility provided with LED indicators to indicate Logic High, Low & Tri-state. <p>5. Bread Board Area</p> <ul style="list-style-type: none"> - Two Distribution Strip of 100 tie points each totaling 200 tie points. - One Terminal Strip of 630 tie points. <p>6. Display</p> <ul style="list-style-type: none"> - Three digit seven-segment LED display. <p>7. Sockets on board</p> <ul style="list-style-type: none"> - 14 Pin Socket- 8Nos. - 16 Pin Socket- 3Nos. - 20 Pin Socket- 1No. <p>8. One IC each of 7400, 7402, 7408, 7432, 7404, 7490, 7495, 7486, 7447 & Two Nos. of IC 7476.</p> <p>9. Power Supplies</p> <ul style="list-style-type: none"> - Fixed DC Power Supply: +5V, ±12V. <p>10. Interconnections</p> <ul style="list-style-type: none"> - All interconnections to be made using 2mm banana Patch cords. <p>All ICS are mounted on IC Sockets. Bare board Tested Glass Epoxy SMOBC PCB to be used.</p>	
3	Bread board	5Nos
4	Wirestripper	3Nos



Principal
COLLEGE OF APPLIED SCIENCE
KUZHALMANNAM, PALAKKAD DL.